

UNITED STATES PATENT APPLICATION

of

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and

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for an

OPTICAL TRANSCEIVER MODULE ARRAY SYSTEM

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OPTICAL TRANSCEIVER MODULE ARRAY SYSTEM

CROSS-REFERENCE TO RELATED APPLICATIONS

[01] This application claims the benefit of U.S. Provisional Application No. 60/426,546, filed November 15, 2002, which is incorporated herein by reference in its entirety.

BACKGROUND OF THE INVENTION

1. The Field of the Invention

[02] The present invention generally relates to optoelectronic modules, including optical transceiver modules. More particularly, the present invention relates to an optoelectronic module array system for minimizing the amount of space required in positioning such apparatus within an optical device.

2. The Related Technology

[03] Fiber optic technology is increasingly employed as a method by which information can be reliably transmitted via a communications network. Networks employing fiber optic technology are known as optical communications networks, and are marked by high bandwidth and reliable, high-speed data transmission.

[04] Optical communications networks employ optical transceivers in transmitting information via the network from a transmission node to a reception node. An optical transceiver at the transmission node receives an electrical signal (containing digital information or other data) from a network device, such as a computer, and converts the

electrical signal via a laser and related components to a modulated optical signal. The optical signal can then be transmitted in a fiber optic cable via the optical communications network to a reception node of the network. Upon receipt by the reception node, the optical signal is fed to another optical transceiver that uses a photodetector and related components to convert the optical signal back into electrical signals. The electrical signals are then forwarded to a host device, such as a computer, for processing. The optical transceivers described above have both signal transmission and reception capabilities; thus, the transmitter portion of an optical transceiver can convert an incoming electrical signal into an optical signal, while the receiver portion of the transceiver can convert an incoming optical signal into an electrical signal.

[05] In a typical transceiver, the components responsible for transmitting and receiving optical signals are located in a transmitting optical sub assembly (“TOSA”) and a receiving optical sub assembly (“ROSA”), respectively. Specifically, the laser and associated components for producing an optical signal are located in the TOSA, while the photodetector and related components for receiving an optical signal are located in the ROSA. Optical signals are introduced to the ROSA and emitted from the TOSA via two respective optical ports defined in the optical transceiver module. The optical ports are typically arranged to be side-by-side when the bottom surface of the transceiver module is mounted within a device.

[06] Various optical devices employ a plurality of optical transceivers to perform the optical transmission and reception operations described above. For instance, optical devices such as switches and optical routers position a plurality of optical transceiver modules together within the device to enable several optical signal channels to be processed

simultaneously. The plurality of optical transceivers are typically placed side-by-side along one or more edges of a host board within the device.

[07] An ever-present goal in the art to increase the number of optical ports available exists when implementing fiber optic systems. This goal extends to optical devices, such as optical switches, routers, and similar devices that utilize optical transceiver modules and other optoelectronic devices having such optical ports.

[08] Though optical transceiver design is largely governed by multi-source agreements (“MSAs”), such as the SFP and XFP MSA standards, which determine the physical dimensions of such modules, it has nonetheless been desirable in the past to reduce the amount of space occupied by the transceiver modules in order to increase optical port density. This is typically achieved in known designs by packing adjacent optical transceiver modules in close proximity to one another on the host board of the optical device and mounting the bottom surfaces of each module to the host board. However, this method alone results in only partial success in increasing optical port density.

[09] In a further effort to increase the optical port density of multiple optical transceiver modules, some known optical device designs mount the bottom surfaces of a first row of adjacent transceiver modules to an upper surface of the host board, and the bottom surfaces of a second row of adjacent transceivers to the host board lower surface. While this configuration can also improve transceiver module density, it nonetheless substantially improves neither inter-module spacing of adjacent transceiver modules nor corresponding optical port density.

[010] In light of the above discussion, a need exists for an optical transceiver module array that overcomes the above challenges. In particular, implementations and methods are

sought by which optical transceiver modules can be positioned so as to maximize the density of optical ports of the transceiver modules within an optical device.

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BRIEF SUMMARY OF THE INVENTION

[011] The present invention has been developed in response to the above and other needs in the art. Briefly summarized, embodiments of the present invention are directed to an optoelectronic module array system for use in optical devices and other environments. The array system of the present invention is configured to increase the density of the optoelectronic modules, such as optical transceiver modules, within the optical device, thereby correspondingly increasing the density of optical ports of the transceiver modules. This results in reduced space requirements within the optical device, and also enables the employment of relatively larger numbers of optical transceiver modules per unit dimension within the optical device or other environment.

[012] In one embodiment, the present array system is composed of various components, including a host board, a plurality of daughter cards mounted to the host board, and a plurality of optical transceiver modules. The host board is positioned within a host device, such as an optical switch, optical router, or other device.

[013] Each of the plurality of daughter cards are positioned parallel to one another in a spaced-apart configuration, then mounted edge-on in a perpendicular orientation with respect to the surface of the host board. A cage is mounted to a like first side of each of the daughter cards.

[014] Each of a plurality of optical transceiver modules, or other type of optoelectronic device, is received into a corresponding one of the cages mounted on each of the daughter cards. The transceiver modules are mounted within the cages of each daughter card in a sideways, or edge-on orientation, wherein an otherwise bottom surface of each transceiver module is positioned adjacent the first side of each daughter card. This orientation in turn positions two optical ports of each transceiver module in a top-over-bottom configuration.

The daughter cards are spaced such that the distance between each transceiver module is minimized.

[015] The configuration described above minimizes inter-module spacing of the transceiver modules, or other optoelectronic device, which in turn increases the density of the transceiver modules in a given space. As a result, optical port density is also increased, which leads to greater signal transfer capacity, smaller optical device sizes, or both.

[016] In one embodiment, release sleeves are employed in connection with optical fiber connectors that pluggably interface with each transceiver module to allow for ready insertion and removal of the connector from the transceiver module.

[017] These and other features of the present invention will become more fully apparent from the following description and appended claims, or may be learned by the practice of the invention as set forth hereinafter.

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BRIEF DESCRIPTION OF THE DRAWINGS

[018] To further clarify the above and other advantages and features of the present invention, a more particular description of the invention will be rendered by reference to specific embodiments thereof that are illustrated in the appended drawings. It is appreciated that these drawings depict only typical embodiments of the invention and are therefore not to be considered limiting of its scope. The invention will be described and explained with additional specificity and detail through the use of the accompanying drawings in which:

[019] Figure 1 is a perspective view of the optical transceiver module array system according to one embodiment of the present invention;

[020] Figure 2 is another perspective view of the optical transceiver module array system of Figure 1;

[021] Figure 3A is an exploded perspective view of a representative optical transceiver module, including a latching mechanism employed in connection with one embodiment of the present invention;

[022] Figure 3B is an assembled view of the optical transceiver module and latching mechanism of Figure 3A;

[023] Figure 4 is a perspective view of the optical transceiver module array system of Figure 1 having optical fibers connected thereto;

[024] Figure 5A is a perspective view of one embodiment of a release sleeve employed in connection with the present invention;

[025] Figure 5B is a top view of the release sleeve of Figure 5A;

[026] Figure 5C is a cross sectional side view of the release sleeve of Figure 5B taken along the lines 5C—5C;

[027] Figure 6A is a perspective view of an optical fiber connector having the release sleeve of Figures 5A-5C slidably connected thereto, wherein the release sleeve is in a disengaged position; and

[028] Figure 6B is a perspective view of the optical fiber connector and release sleeve of Figure 6A, wherein the release sleeve is in an engaged position.

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DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[029] Reference will now be made to figures wherein like structures will be provided with like reference designations. It is understood that the drawings are diagrammatic and schematic representations of presently preferred embodiments of the invention, and are not limiting of the present invention nor are they necessarily drawn to scale.

[030] Figures 1-6B depict various features of embodiments of the present invention, which is generally directed to an optical transceiver module array system. The present system can facilitate increased optical port density by providing an improved small-separation placement scheme among a plurality of optical transceiver modules within an optical device, such as an optical switch, optical router, or other optical device. Note that the configurations of the present invention to be shown and described herein and in the accompanying figures are merely examples of the broader principles taught by the present invention. Accordingly, though the present invention will be described in connection with optical transceiver modules, it is appreciated that a variety of other optoelectronic devices can also benefit from the teachings discussed herein.

[031] Reference is first made to Figures 1 and 2, which depict various features of one embodiment of the present invention. Specifically, Figures 1 and 2 show an optical transceiver array system (“array system”), generally designated at 10. The array system 10 has various components, including a host board 12, a plurality of daughter cards 14, a plurality of cages 16, and a plurality of optical transceiver modules (“transceivers”) 20. Each of these components cooperates to provide a high density transceiver arrangement that is beneficial in increasing the number of transceiver optical ports for a given space, as will be seen.

[032] The host board 12 includes a top mounting surface 22 that provides a surface upon which the other components of the system array 10 can be disposed. The host board 12 can have one of a variety of areal dimensions, and can be included within an optical device (not shown), such as an optical switching device, optical router, computing device or the like, or can be self-supporting. The host board 12 can further include a variety of electrical interconnects and conductive pathways for providing electrical interfaces between the optical device and the transceivers 20, as will be discussed.

[033] The host board 12 is connected to each of the plurality of daughter cards 14, as shown in Figures 1 and 2. In particular, each daughter card 14 is mounted edge-on with the host board 12 such that a planar first side 14A of each daughter card is perpendicular to the host board top mounting surface 22. The daughter cards 14 are mounted parallel to and in series with one another in a spaced-apart configuration along an edge of the host board 12, as seen in the figures. The separation between adjacent daughter cards 14 is such that the spacing between adjacent optical transceivers 20 is minimized, as discussed below. The daughter cards 14 can be composed of any suitable material to allow mounting of other array components, as described below.

[034] The first side 14A of each daughter card 14 provides a mounting surface for a respective one of the plurality of cages 16. Composed of metal or other suitable material, each cage 16 is coupled in the present embodiment to its respective daughter card 14 on the first side 14A thereof. In other embodiments, however, it is possible to mount the cage to the host board 12, either in addition or alternatively to the daughter card mounting. Each cage 16 in the present embodiment defines a hollow rectangular volume and includes open first and second ends 16A and 16B, respectively.

[035] The shape of the cages 16 is configured as described above to provide a receptacle for each of the plurality of transceivers 20, as seen in Figures 1 and 2. In detail, each of the transceivers 20 has a first end 20A and a second end 20B, and is received through the first end 16A of a respective one of the cages 16 to seat therein. In this position, each transceiver 20 is received within the respective cage 16 and daughter card 14 in a specified orientation with respect to the host board 12. Further, each transceiver 20 is electrically connected to the host board 12 via the daughter card 14. Specifically, the first side 14A of each daughter card 14 includes a connector receptacle 24 positioned near the second end 16B of each cage 16 for receiving a card-edge connector 26 (Figures 3A, 3B) extending from the second end 20B of the transceiver 20. Each connector receptacle 24 is then electrically connected via conductive pathways and electrical interfaces on the daughter card 14 (not shown) to the host board 12, as appreciated by those skilled in the art. In this configuration, the transceivers 20 are referred to as “pluggable” transceivers. In other embodiments, however, the transceivers can include pins extending therefrom, wherein the pins interconnect with the daughter card and/or the host board.

[036] In addition, each cage 16 is configured to provide a ground plane for each transceiver 20, as well as serve as a supplemental shield, in addition to the outer shell of the transceiver, against electromagnetic interference (“EMI”). In addition to these connections, other conductive and electrical schemes can be employed in connection with the present invention. For example, in one embodiment electrical connection between the transceiver and the host board can be established directly from the transceiver to the host board.

[037] The plurality of cages 16 shown in Figures 1 and 2 are commercially available. However, it is appreciated that custom cages can be configured and employed in the present system. One example of a custom cage is depicted in Figure 4, which shows a three-sided

cage that further reduces transceiver spacing. Moreover, in one embodiment a multi-slot custom cage can be configured to serve the functionality of both the cage and the daughter card of Figures 1 and 2. In such a case, the daughter cards can be eliminated from the system. In yet another embodiment, cages can be mounted to either side of the daughter cards, if desired, or one side of a daughter card can be configured and sized to support multiple cages. These and other similar modifications to the present invention are therefore contemplated.

[038] Each transceiver 20 in the present embodiment that is received into one of the cages 16 includes two optical ports 28 and 30 formed on an optical interface portion 32 that is located at the first end 20A of the transceiver. The optical ports 28 and 30 respectively correspond to a transmission optical subassembly (“TOSA”) and a receiving optical subassembly (“ROSA”) (not shown) that are contained within each transceiver 20. As shown, the orientation of each transceiver within the cages 16 represents an approximate 90° rotation from transceiver orientations in known transceiver implementations. This orientation places each transceiver edge-on such that its width dimension is perpendicular to the top mounting surface 22 of the host board 12. This results in the optical ports 28 and 30 being positioned in a top-over-bottom vertical configuration such that an imaginary line passing through the central portions of the optical ports intersects the host board 12 substantially perpendicularly.

[039] It is noted that, in Figures 1 and 2, four sets of optical transceivers 20, cages 16, and daughter cards 14 are included in the array system 10. However, it is appreciated that the present system is scalable such that a greater or lesser number of sets of these components can be included. In one embodiment, for example, 48 optical transceivers can be included in an array system configured according to the present invention. Such an array system will,

because of the present design, substantially increase optical port density on the host board. It is further noted that the transceivers in Figure 1 are all oriented alike. However, in other embodiments each transceiver can have a differing orientation with respect to the other transceivers.

[040] In the present embodiments, the transceivers shown in Figures 1 and 2 conform to the SFP Multi-Source Agreement (“MSA”). However, transceivers conforming to one or more of a variety of form factors, such as the XFP standard, can also be employed with the present system. Additionally, transceivers of differing form factors can be positioned together in the same array system, if desired.

[041] As mentioned, because of the edge-on transceiver orientation shown in Figures 1 and 2, a compact transceiver array geometry is achieved by the present array system, which results in increased optical port density in the array system. These features beneficially result in enhanced functionality for the optical device (not shown) that contains the present array, as well as reduced effort and expense in the manufacturing of the optical device.

[042] Together with Figures 1 and 2, reference is now also made to Figures 3A and 3B, which depict various features of a latching mechanism 40 that is included with each transceiver 20 of the embodiment shown in Figures 1 and 2. The latching mechanism 40 is employed to releasably secure each transceiver 20 within its designated location in the array system 10, thereby enabling each transceiver to be selectively inserted into and removed from the array system 10 when desired.

[043] As partially seen in Figures 1 and 2, the latching mechanism 40 is located on a side surface 20C of each transceiver 20, at the optical interface portion 32 thereof. For purposes of illustration, the transceiver 20 shown in Figures 3A and 3B is rotated approximately 90° counter-clockwise from the orientation of the transceivers shown in Figures 1 and 2 to

enable a full view of the latching mechanism 40 to be realized. Note that the transceiver 20 of Figures 3A and 3B is representative of any one of the transceivers shown in the array system 10 of Figures 1 and 2.

[044] Figures 3A and 3B depict various components of the latching mechanism 40, including a bail 42 and a pivot block 44. The bail 42 in the illustrated embodiment is a shaped piece of rigid wire. The bail 42 has various segments, including a first end 46, a V-shaped angled first portion 48, a first shoulder 50, a cam 52, a second shoulder 54, a V-shaped angled second portion 56, and a second end 58. The bail 42 is so shaped as to allow it to operate as a user-actuated portion for inserting and removing the transceiver 20 from the array system 10.

[045] The first end 46 of the bail 42 is received into a curved recess 60 that is defined in a top surface 20D, while the second end 58 of the bail is received into a correspondingly curved recess 62 defined on a bottom surface 20E. The curved recesses 60 and 62 enable movement of the first and second bail ends 46 and 58 to occur without exposure of the ends to other surfaces so as to prevent possible snagging thereof.

[046] The first and second shoulders 50 and 54 of the bail 42 are received into corresponding retention slots 64 and 66 defined on the transceiver side surface 20C such that the cam 52 is interposed between the retention slots and such that pivoting of the bail 42 about the retention slots can occur. The pivot block 44 is positioned over the cam 52 of the bail 42, and is received into corresponding pivot points 68 and 70 defined in the transceiver side surface 20C. The pivot block 44 includes a lock pin 72 that moveably extends through a hole 75 defined in an outer housing 76 of the transceiver 20. A leaf spring 74 defined from the outer housing 76 on the transceiver side surface 20C compressively engages the

pivot block 44 to secure the pivot block while enabling it to pivot about the pivot points 68 and 70.

[047] Figure 3B shows the latching mechanism 40 as assembled as part of the transceiver 20. As noted above, the latching mechanism 40 of each transceiver 20 releasably secures the transceiver in position within the array system 10 shown in Figures 1 and 2. In detail, Figure 3B shows the latching mechanism 40 in a latched configuration, wherein the bail 42 is positioned such that the cam 52 thereof causes no pivoting of the pivot block 44 to occur. In this latched configuration, the leaf spring 74 biases the pivot block 44 such that the lock pin 72 extends through the hole 75 in the outer housing and engages a hole 78 of the cage 16 (Figure 2) when the transceiver is positioned in the array system 10, as shown in Figure 1. Alternatively, the hole 78 can be defined in another structure, such as the daughter card 14.

[048] To disengage one of the transceivers 20 from position within the array system 10, the bail 42 is rotated toward the first end 20A of the transceiver. This can be accomplished by a user grasping the bail 42 at the first and second angled portions 48 and 56 to cause the rotation. This in turn causes the first and second bail ends 46 and 58 to move from their positions shown in Figure 3B and travel along the curved recesses 60 and 62 as the bail is swung out toward the transceiver first end 20A. This movement of the bail 42 causes the bail cam 52 to cause the pivot block 44 to pivot such that the lock pin 72 disengages from the hole 78 in the cage 16. After disengagement of the lock pin 72 from the cage hole 78, the transceiver 20 can be removed from the array system by a pulling force from a user on the bail 42.

[049] When it is desired to insert one of the transceivers 20 into the array system 10, a user can rotate the bail 42 away from the first end 20A of the transceiver by grasping the bail at the first and second angled portions 48 and 56. This action disengages the bail cam 52 from

the pivot block 44, which fully extends the lock pin through the hole 75 defined in the transceiver outer housing 76. The transceiver 20 can then be inserted into its place, at which point the lock pin 72 can engage the hole 78 of the cage, thereby locking the transceiver in place. In this way, each transceiver can be selectively and individually inserted into and removed from the array system as desired. Further details regarding the latching mechanism described herein can be found in U. S. Patent No. 6,439,918 B1, entitled "Electronic Module Having an Integrated Latching Mechanism," filed October 4, 2001, which is incorporated herein by reference in its entirety. Notwithstanding the above discussion, however, it is appreciated that latching mechanisms having other structures and configurations can also be employed in embodiments of the array system. Moreover, a latching mechanism that is employed with the array system described herein can be located in locations other than that shown and described above.

[050] Reference is now made to Figure 4. As shown, the array system 10 includes the four transceivers 20 that are each adjacent one of the daughter cards 14 described above, and positioned on the host board 12. Note that the array system 10 includes cages 116 that are three-sided, in contrast to the four-sided cages 16 shown in Figures 1 and 2. The cages 116 can be used in embodiments of the present invention to further minimize spacing between the transceivers 20. In this configuration, then, a side surface 20F of the left-most transceiver 20 is shown.

[051] Figure 4 additionally shows first and second connectorized optical fiber sets 80 and 82 that are each in differing connective states with the optical interface portion 32 of two of the transceivers 20 of the array system 10. In detail, the first connectorized optical fiber set 80 is shown fully coupled with the optical interface portion 32 of the respective transceiver 20 such that first and second optical fibers 80A and 80B of the set are optically coupled with

the optical ports 28 and 30 of the transceiver. In contrast, the second connectorized optical fiber set 82 is shown partially coupled with the optical interface portion 32 of its respective transceiver 20. As such, the first and second optical fibers 82A and 82B are not yet fully coupled with optical ports 28 and 30, respectively.

[052] Each of the first and second connectorized optical fiber sets 80 and 82 includes a standard LC duplex connector that is fitted with a release sleeve 86 that facilitates engagement and disengagement of the optical fiber sets to and from respective transceivers 20. Such engagement and disengagement can otherwise be difficult to achieve, given the reduced clearance between the optical ports 28 and 30 of adjacent transceivers 20 in the array system 10. Thus, the release sleeves 86 facilitate ease of operation with regard to the array system 10.

[053] Reference is now made to Figures 5A-5C in describing various details regarding the release sleeve 86. In particular, the release sleeve 86 includes a substantially rectangular body 88 having a raised portion 90. In the illustrated embodiment, the raised portion is laterally more narrow than the body 88. The body 88 and raised portion 90 thereof cooperate to define a first open end 92, while the body also defines a second open end 94. The bottom portion of the body 88 is also open. Opposing lateral sides of the body 88 include extended portions 96 to assist in handling the release sleeve 86. Two lips 98 extend inwardly toward one another from a bottom edge 100 of the body 88 for engaging the LC duplex connector 104 (Figure 6A).

[054] As best seen in Figure 5C, the raised portion 90 defines a complexly curved top inner surface 102. This curved top inner surface 102 includes both convex and concave curve portions to form a cam surface that is used to selectively engage and disengage portions of the connectorized optical fiber sets, as will be seen below.

[055] Figure 6A shows the release sleeve 86 slidably attached to a standard LC duplex connector 104, such as that used in Figure 4 to optically connect with the optical ports 28 and 30 (Figure 1) of one of the transceivers 20. The release sleeve 86 in Figure 6A is shown in a disengaged position that is used when the LC duplex connector 104 is fully coupled with one of the transceivers 20 of the array system 10. In such a coupled position, the LC duplex connector 104 is received into the optical interface portion 32 of one of the transceivers 20 such that two connector tips 106 optically couple with the optical ports 28 and 30 of the transceiver. This causes two resilient clips 108 of the LC duplex connector 104 to engage with corresponding notches 110 (Figures 1, 2) formed on the optical interface portion 32 of the respective transceiver 20 to lock the LC duplex connector in a specified position.

[056] As stated, the release sleeve 86 is shown in Figure 6A in a disengaged position, wherein the release sleeve is slid along the LC duplex connector body in an axial direction away from connector tips 106. This position prevents substantial contact between the curved top inner surface 102 (Figure 5C) of the release sleeve raised portion 90 and a connector latch 112 of the LC duplex connector 104. Correspondingly, the connector latch 112 is shown disengaged from adjacent ends of each resilient clip 108. This configuration ensures that a secure fit between the transceiver optical interface portion 32 and the LC duplex connector 104 is achieved.

[057] In contrast, Figure 6B shows the release sleeve in an engaged position, which is used to retract the LC duplex connector 104 from engagement with the optical interface portion 32 of the respective transceiver 20 of the array system 10. This position is achieved by sliding the release sleeve 86 along the LC duplex connector body in an axial direction toward the connector tips 106. This axial movement causes the curved top inner surface 102

of the release sleeve raised portion 90 to engage the connector latch 112 and force it into contact with each resilient clip 108 of the LC duplex connector 104. The resilient clips 108 in turn are forced downward to disengage each clip from the notches 110 of the respective transceiver 20. At this point, the LC duplex connector 104 can be removed from engagement with the optical interface portion 32 of the transceiver 20. Thus, it is seen that the release sleeve 86 enables insertion and removal of the LC duplex connector by virtue of its axial position with respect to the LC duplex connector, thereby obviating the need for direct manual manipulation of the LC duplex connector. Each transceiver of the array system can therefore be connected with an LC or other similar optical fiber connector via respective release sleeves as shown herein. Of course, the release sleeve can be adapted to operate with single or duplex connectors.

[058] The release sleeve can be composed of any suitable material that allows it to function as described above. In one embodiment, the release sleeve is composed of injection molded thermoplastic. In another embodiment, the thermoplastic of the release sleeve further includes a specified percentage of glass fibers intermixed therewith. Alternatively, a variety of other resins can be used.

[059] In addition to the above description, it is appreciated that the release sleeve can be adapted to operate with optical fiber connectors having physical characteristics that vary from that shown here. Moreover, the release sleeve can include designs or features in addition or alternative to those described herein.

[060] It is noted that the array system shown in Figure 1 includes transceivers mounted on one surface of the host board. In other embodiments, the array system can be expanded to include transceivers similarly packaged as shown in Figure 1, but located on the opposing surface of the host board, in a “belly-to-belly” configuration to even further expand the

number of transceivers of the host board. These and other modifications to the present invention are therefore contemplated as residing within the claims of the present invention.

[061] The present invention may be embodied in other specific forms without departing from its spirit or essential characteristics. The described embodiments are to be considered in all respects only as illustrative, not restrictive. The scope of the invention is, therefore, indicated by the appended claims rather than by the foregoing description. All changes that come within the meaning and range of equivalency of the claims are to be embraced within their scope.

What is claimed is:

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